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On January 10, 2003

TOWNSEND and TOWNSEND and CREW LLP

By: 

Michelle R. Offenbaker

PATENT
Attorney Docket No.: 021111-000100US

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Howard G. Sachs

Application No.: 09/840,747

Filed: April 23, 2001

For: CIRCUIT GROUP DESIGN
METHODOLOGIES

Examiner: Brandon Bowers

Art Unit: 2825

AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action mailed September 11, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend claim 11 as follows:

11. (Amended) A method of determining a definition of a physical representation of at least a portion of an integrated circuit, the integrated circuit performing logic operations, arithmetic operations, control operations, and memory operations, the integrated circuit being comprised of a plurality of groups, the groups being largely comprised of between 300 and 5000 gates, the groups being present in a